

205220-TT21800T

In re Application of:
MORI, FUMIO
Divisional of Appln. No.
09/181,693
For: Integrated Circuit Mounting
Structure And Its Mounting Method
Thereof

1/3

FIG. 1

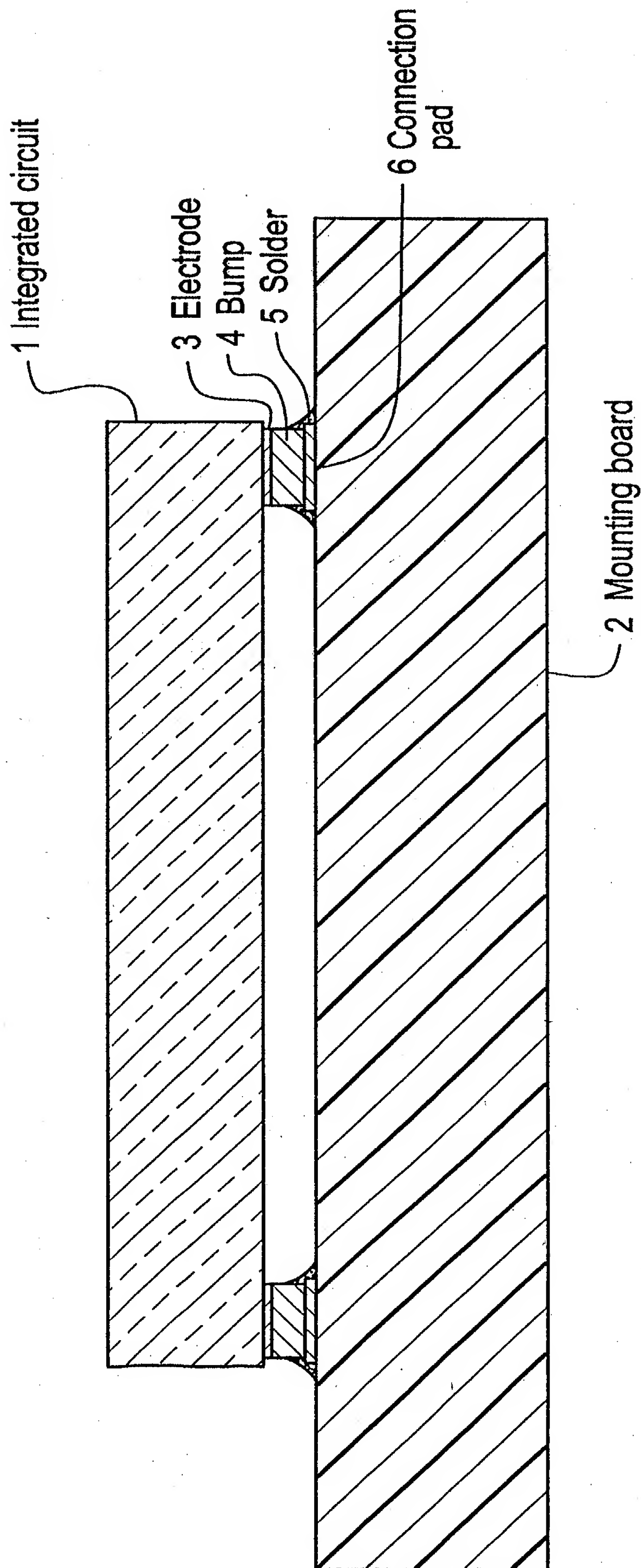


FIG. 2A

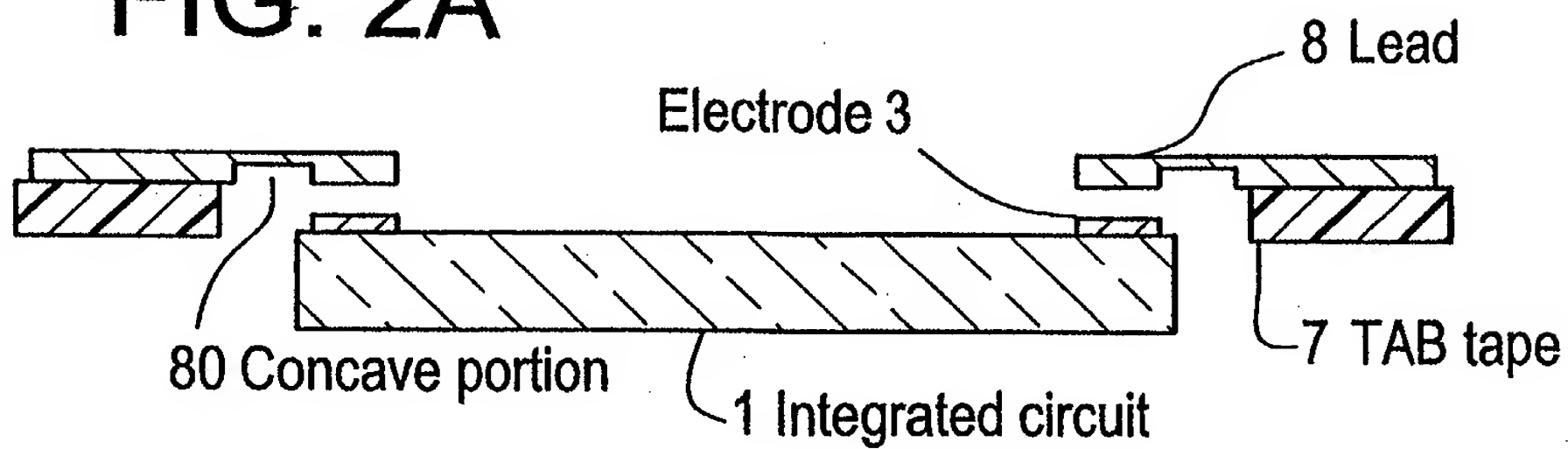


FIG. 2B

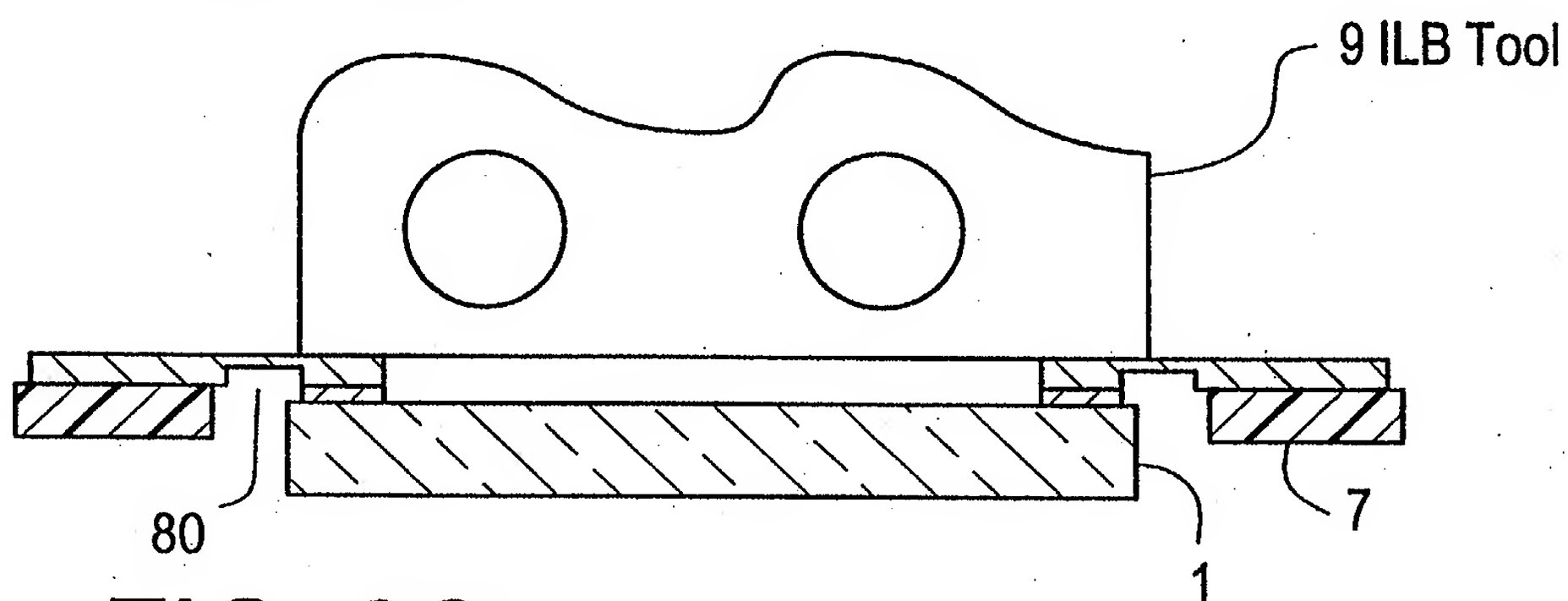


FIG. 2C

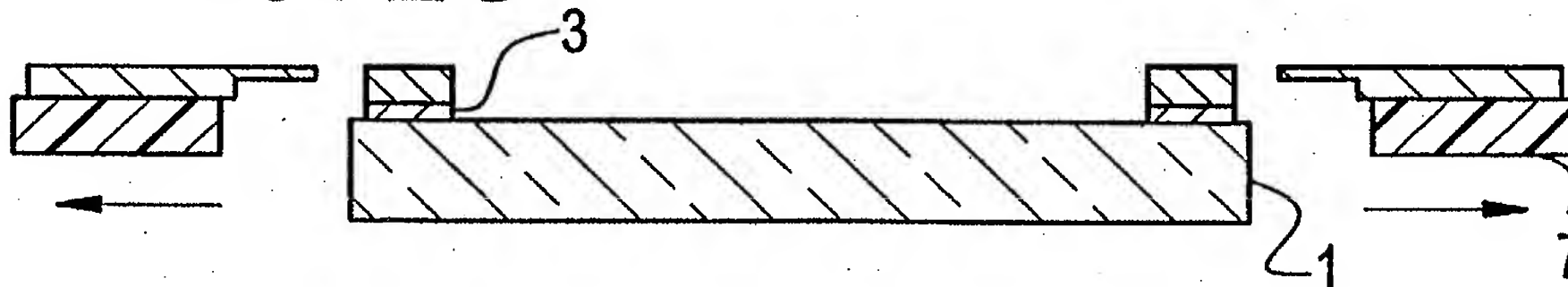


FIG. 2D

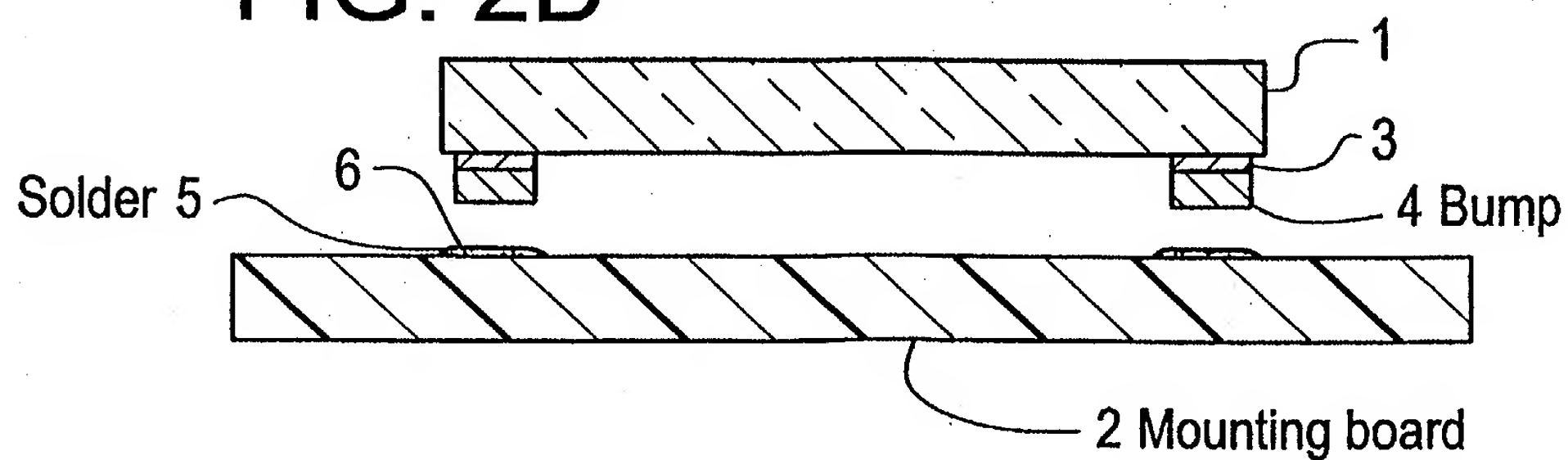


FIG. 2E

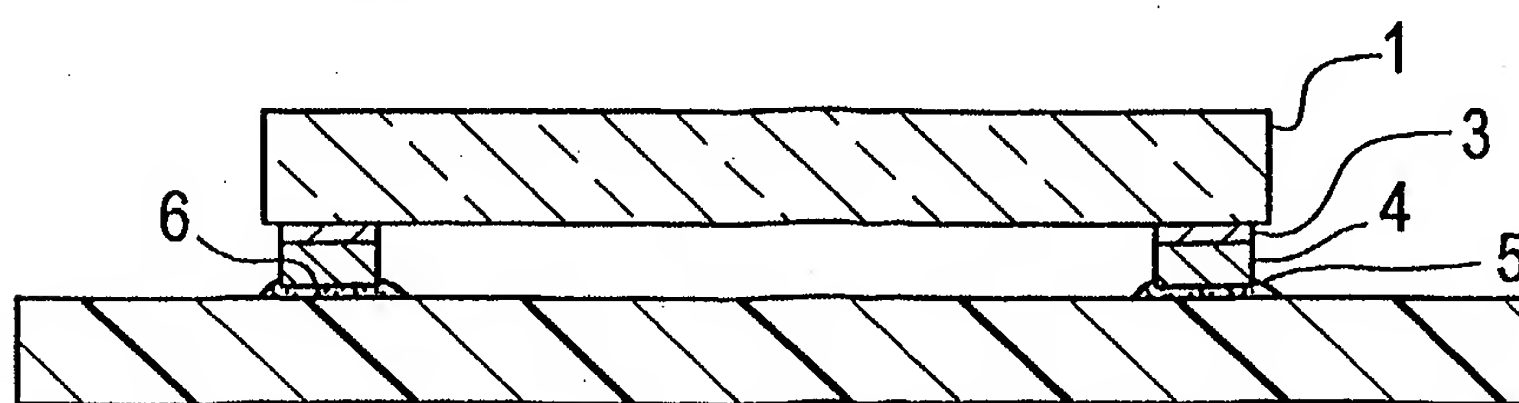


FIG. 3A

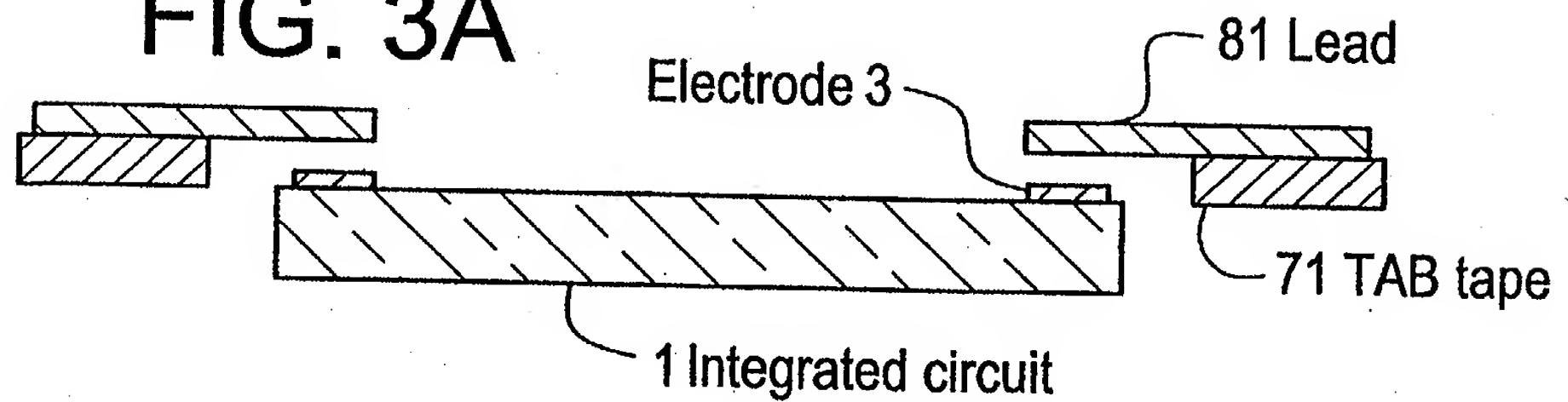


FIG. 3B

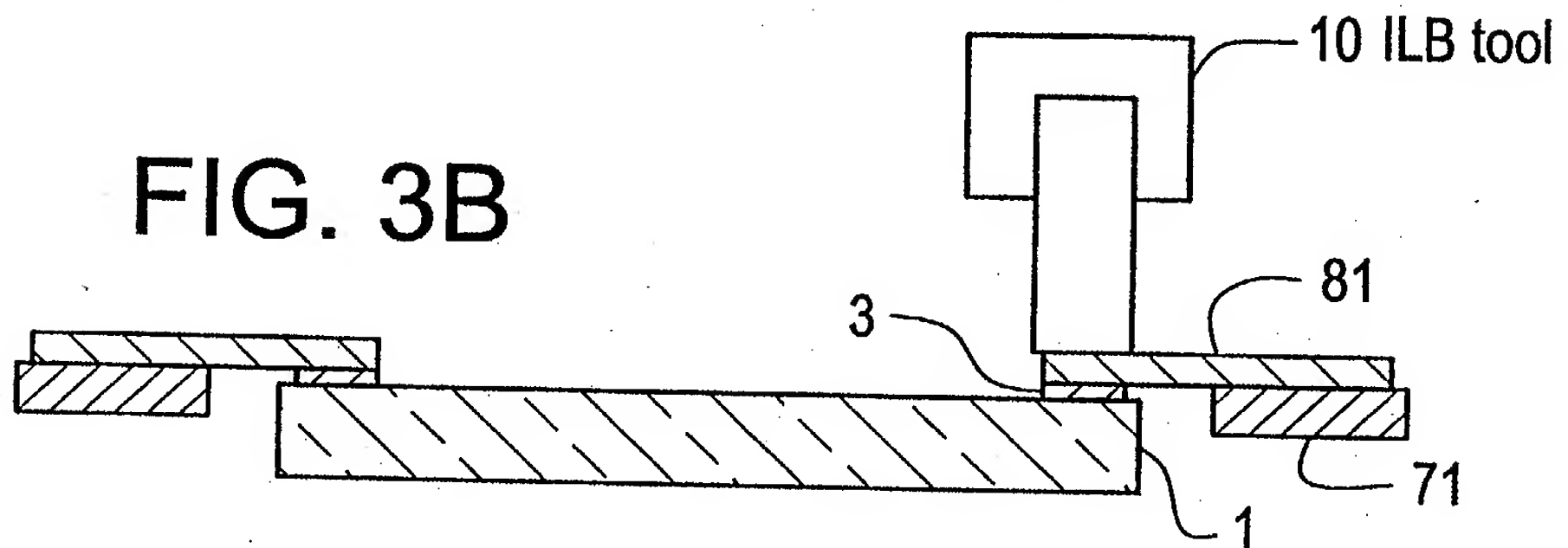


FIG. 3C

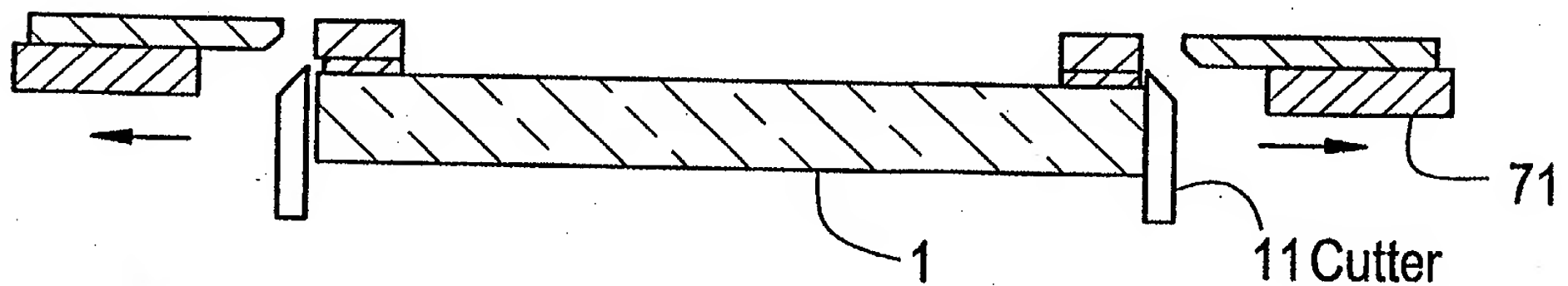


FIG. 3D

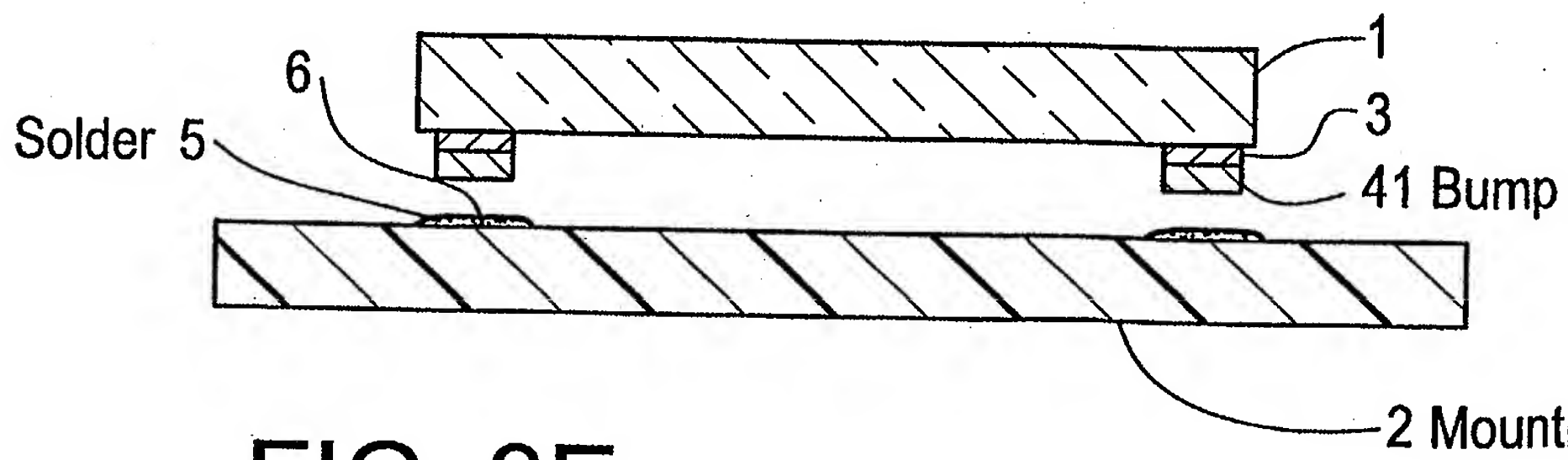


FIG. 3E

